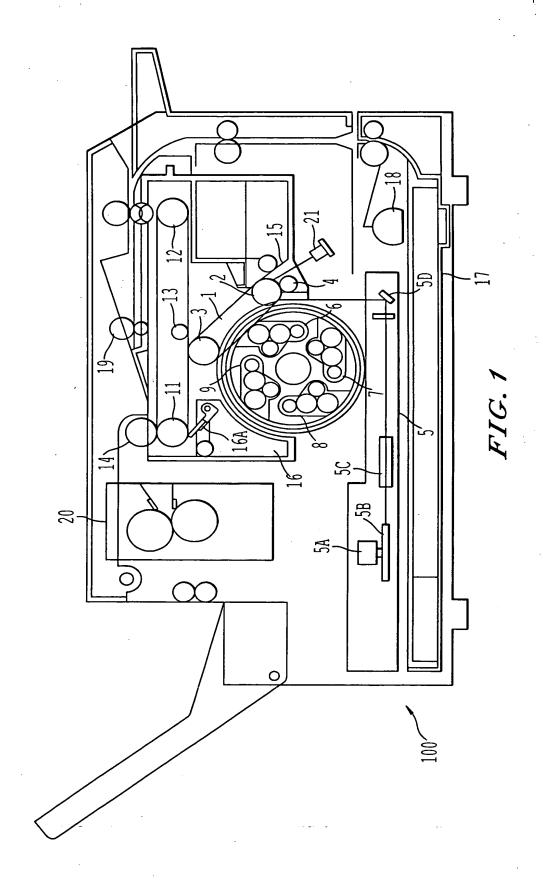
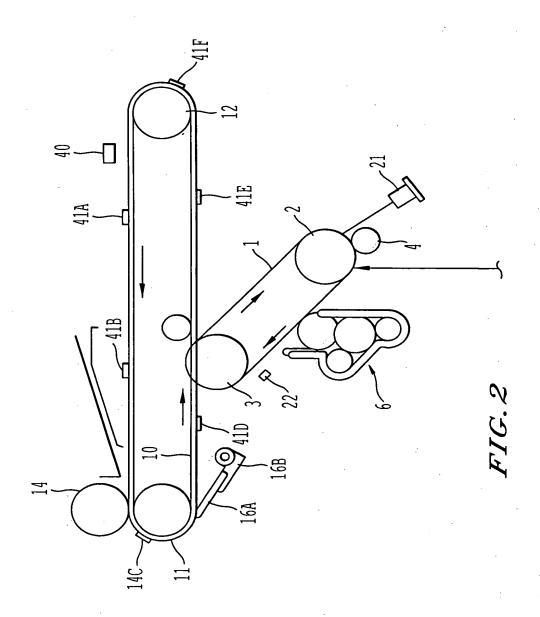
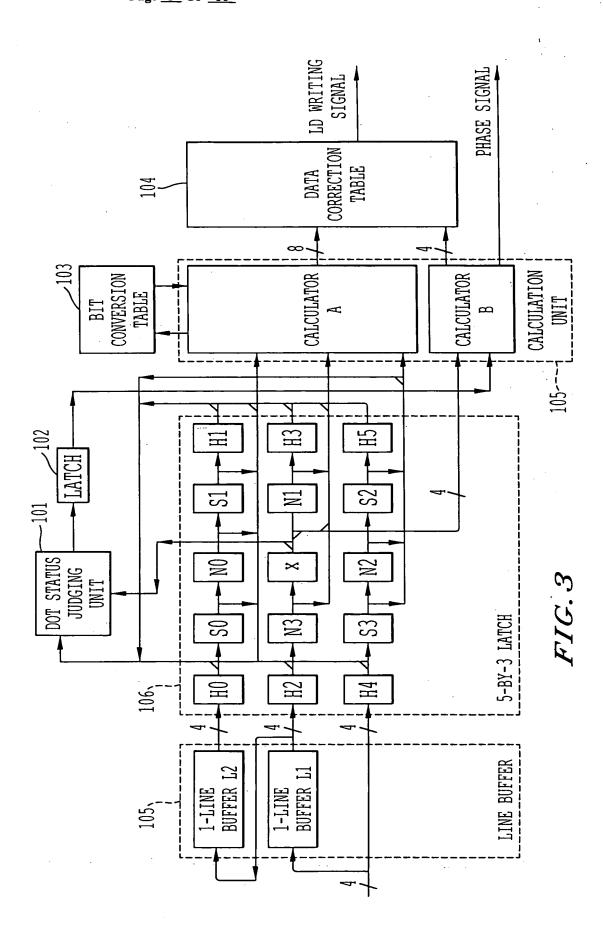
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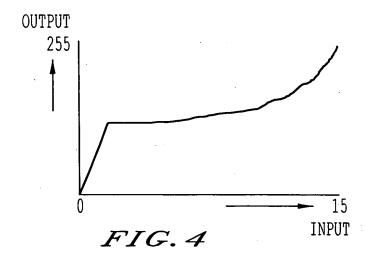
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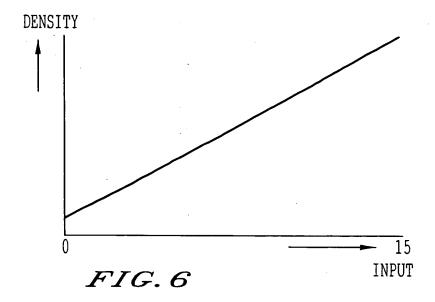
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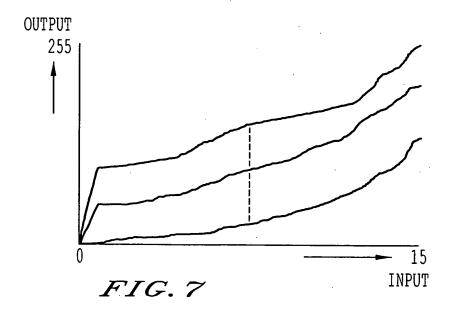


INPUT	OUTPUT
0	0
1	77
2	78
3	81
4	83
5	87
6	92
7	98
8	105
9	114
10	126
11	142
12	161
13	185
14	216
15	255

FIG. 5

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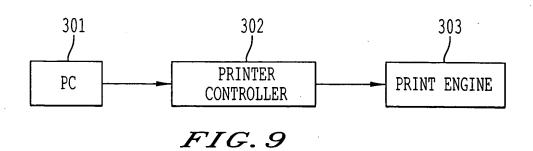
MODE 0

RIGHT SIDE

LEFT SIDE

MODE 1

FIG. 8



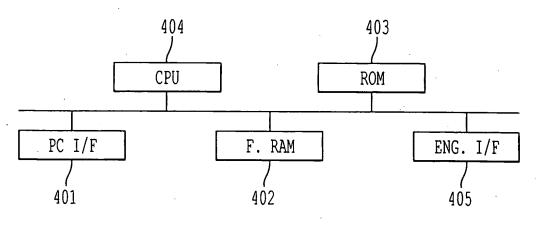


FIG. 10

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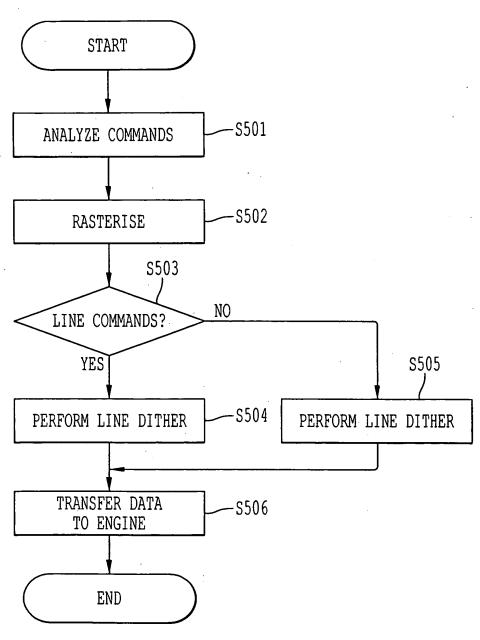


FIG. 11

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1	1	1	1
1	1	1	1
1	1	1	1
1	1	1	1

FIG. 12A

19		9	12	16
21		3	6	24
13		15	18	10
7	-	25	22	4

FIG. 12B

42	32	35	39
44	26	29	47
36	38	41	33
30	48	45	27

FIG. 12C

34	13	19	28
37	1	7:	43
22	25	31	16
10	46	40	4

FIG. 13A

35	14	20	29
38	2	8	44
23	26	32	17
11	47	41	5

FIG. 13B

36	15	21	30
39	3	9	45
24	27	33	18
12	48	42	6

FIG. 13C

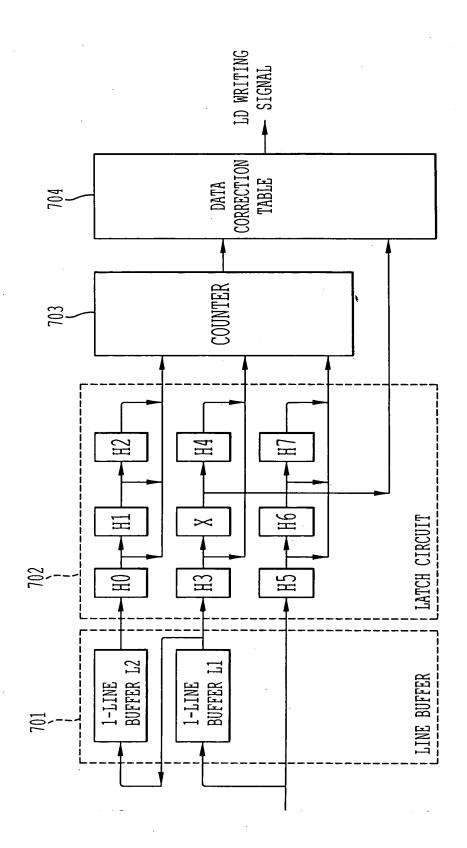


FIG. 14

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	LD WRITING VALUE		
INPUT DATA	0~7	8	
1	85	30	
2	170	80	
3	255	255	

FIG. 15